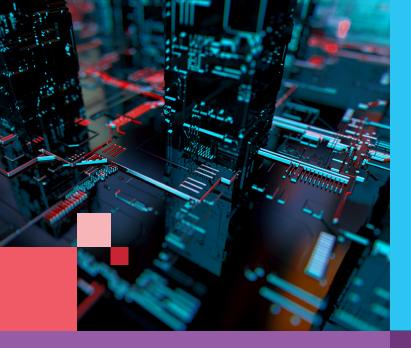
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Analog, RF and mixed-signal circuit design, physical design and component verification – this is the main field of Intel Austria and its top-class experts.

Our team in Villach provides market-leading solutions of analog/mixed-signal IP (ADC/DAC/PLL) for 5G base station products.



Cornelia Ortner cornelia.ortner@intel.com Tel. +43 4242 36732 047

Intel Austria GmbH Europastraße 8 T02 9524 Villach

